

ABSTRACT OF THE DISCLOSURE

A line memory interpolates interlace video signals between horizontal lines to generate current-field video signals of one frame. A field memory stores the current-field video signals until input of the next field, and interpolates video signals between horizontal lines in the previous field to generate previous-field video signals of one frame. In the current-field video signals and previous-field video signals, an arithmetic circuit refers to video signals corresponding to the same pixels, so as to generate correction video signals for these pixels. By thus driving a group of pixels of one frame on a field basis, luminance can be increased. Further, by modulating the driving signals based on video signals of the previous field, a response speed of the pixels can be increased. Despite these advantages, modulation error will not be caused by mispairing of calculated video signals, thereby providing a display device with good display quality.